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10/074,003
P12663**BEST AVAILABLE COPY***Amendments***In the Claims:**

The claims are to be amended as follows:

- 1-9. (Previously Cancelled).
10. (Currently Amended) A space transformer comprising:
a silicon medium; and
a predetermined contact pattern comprising electrically conductive material disposed in an inner region of the substrate and defining electrical contact zones located to provide double-sided electrical contacts for the space transformer, the contacts comprising land grid array side contacts and semiconductor side contacts, said land grid array side contacts having dimensions and spacing less larger than said semiconductor side contacts.
11. (Original) The space transformer according to claim 10, wherein the silicon medium comprises a first silicon layer and a second silicon layer, the contact pattern being disposed between the first silicon layer and the second silicon layer.
12. (Previously Amended) The space transformer according to claim 11, wherein the second silicon layer defines at least one via therein, at least some of the electrically conductive material being located in the at least one via.

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13. (Original) The space transformer according to claim 11, further comprising an adhesion promoter disposed between the electrically conductive material and the first silicon layer.
14. (Currently Amended) A space transformer comprising:
- a first silicon layer defining a plurality of vias therein;
 - a predetermined contact pattern comprising copper on the first silicon layer, at least some of the copper being disposed in the plurality of vias for defining thermal contact zones in the plurality of vias to provide double-sided electrical contacts for the space transformer; and
 - a second silicon layer disposed on the contact pattern, the contact pattern being disposed in an inner region located between the first silicon layer and the second silicon layer, the contacts comprising land grid array side contacts and semiconductor side contacts, said land grid array side contacts having dimensions and spacing less larger than said semiconductor side contacts.
15. (Original) The space transformer according to claim 14, further comprising a layer of adhesion promoter disposed between the electrically conductive material and the first silicon layer.
16. (Currently Amended) A space transformer comprising:
- a first silicon layer;

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